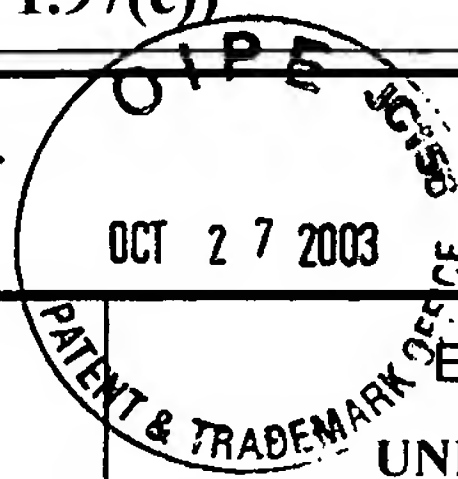


**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
**(Under 37 CFR 1.97(b) or 1.97(c))**

Docket No.  
**BUR920030025US1**

In Re Application Of: **ROBERT GEFFKEN ET AL.**



Serial No.  
**10/605,440**

Filing Date  
**09/30/2003**

Examiner  
**UNKNOWN**

Group Art Unit  
**UNKNOWN**

Title: **ADJUSTABLE SELF-ALIGNED AIR GAP DIELECTRIC FOR LOW CAPACITANCE WIRING**

Address to:  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

**37 CFR 1.97(c)**

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

**OR**

☐ the fee set forth in 37 CFR 1.17(p).

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.  
**BUR920030025US1**

In Re Application: **ROBERT GEFFKEN ET AL.**

OCT 27 2003

Serial No.

10/605,440

Filing Date

09/30/2003

Examiner

UNKNOWN

Group Art Unit

UNKNOWN

**ADJUSTABLE SELF-ALIGNED AIR GAP DIELECTRIC FOR LOW CAPACITANCE WIRING**

**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of \_\_\_\_\_ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. **09-0456**  
as described below.
- ☐ Charge the amount of \_\_\_\_\_
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**Anthony J. Canale**  
Signature

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Dated: **10/23/2003**

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INFORMATION DISCLOSURE CITATION  
(Use several sheets if necessary)

Docket Number (Optional)  
BUR920030025US1

Application Number  
10/605,440

Applicant(s)  
Robert Geffken et al.

Filing Date  
09/30/2003

Group Art Unit  
Unknown

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		V. Arnal, J. Torres, P. Gayet, R. Gonella, P. Spinelli, M. Guillermet, J-P Reynard, GC. Verove; "Integration of a 3 Level CuSiO2 Air Gap Interconnect for Sub 0.1 micron DMOS Technologies;" IEEE, 6/01, pages 298-300.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.